

Fig. 1

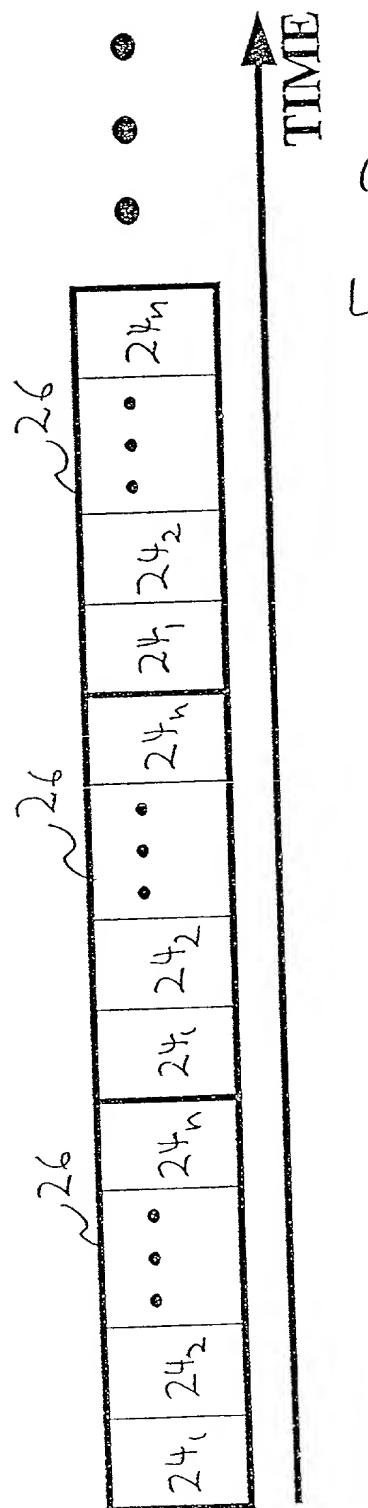


Fig. 2

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CHANNEL	TIME SLOT			
	S1	S2	...	SN
CODES				
CODE 1				
CODE 2				
:				
CODE M				

Fig. 3

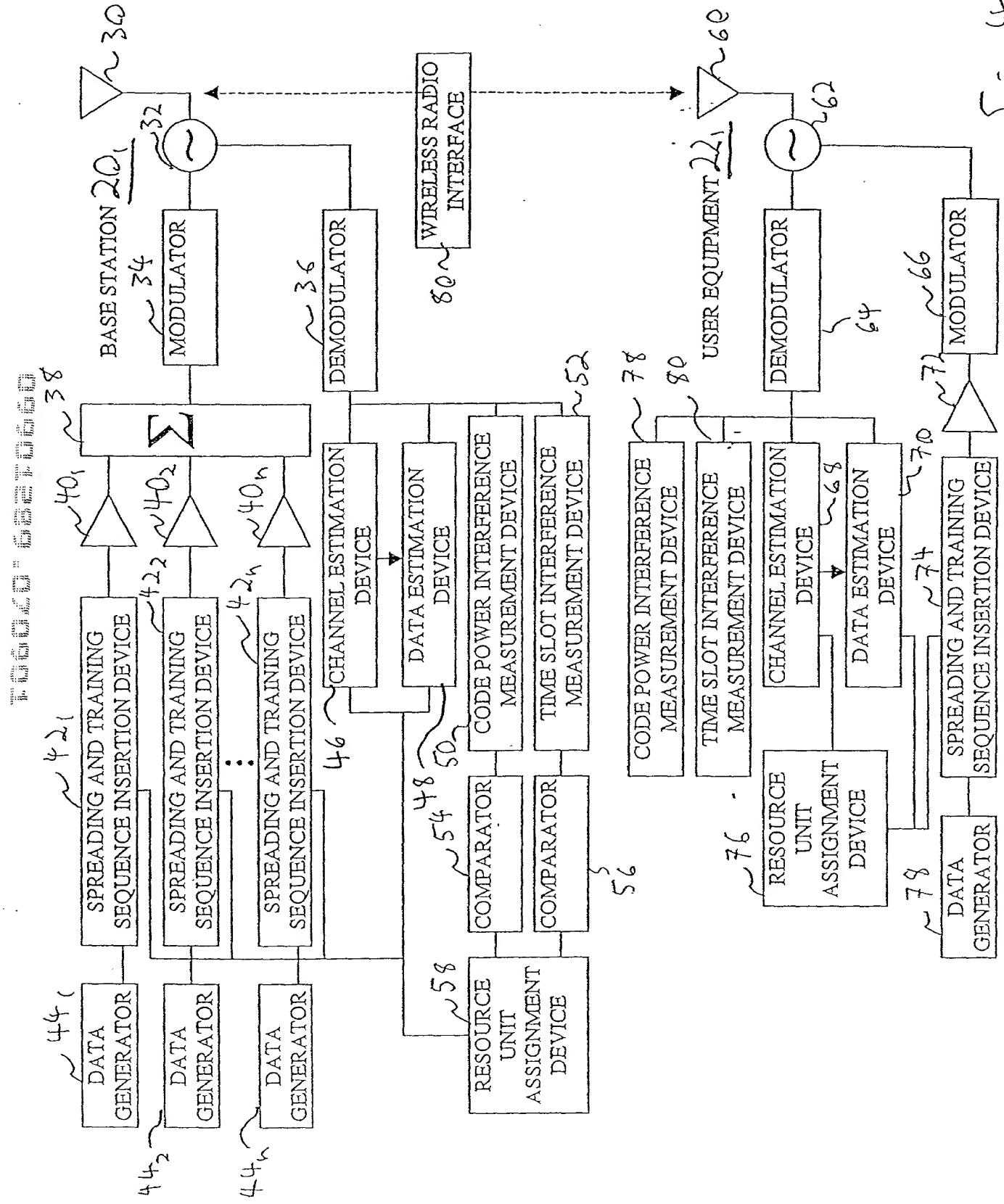


Fig. 4

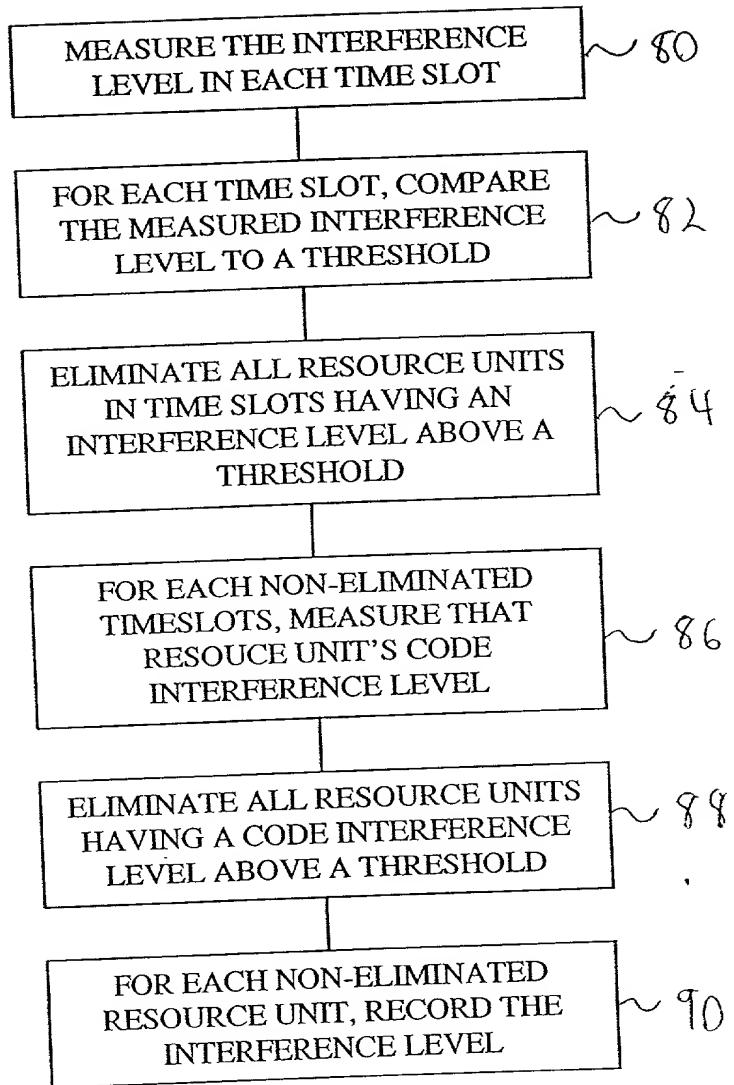


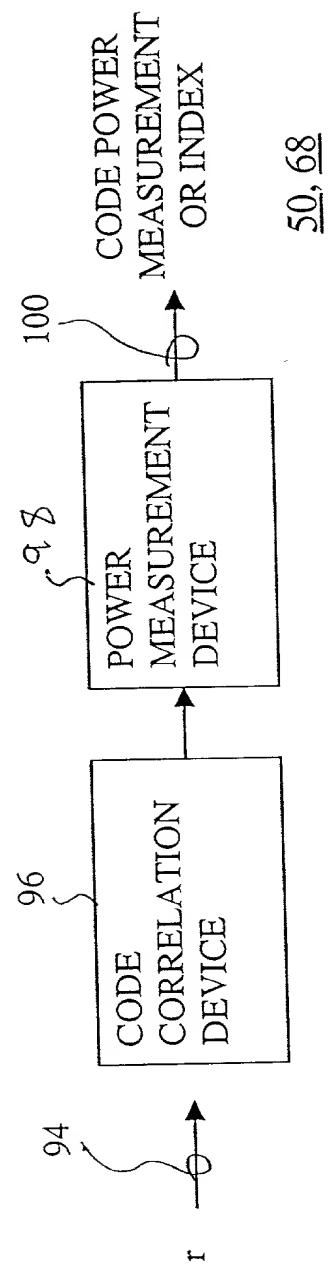
FIG. 5

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CHANNEL CODE	PREFERENCE MATRIX														
	TIME SLOT														
	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13	S14	S15
CODE 1	X	x	x	X	5	6	4	x	8	X	8	8	8	x	X
CODE 2	X	x	1	X	x	4	x	x	x	X	7	5	x	x	X
CODE 3	X	x	3	X	x	5	x	x	x	X	8	4	x	4	X
CODE 4	X	5	3	X	x	x	x	x	6	X	8	6	x	4	X
CODE 5	X	5	x	X	x	x	x	x	5	X	10	6	x	x	X
CODE 6	X	5	x	X	x	x	4	x	5	X	x	8	3	x	X
CODE 7	X	5	x	X	x	5	3	x	4	X	x	5	3	x	X
CODE 8	X	4	x	X	x	5	x	x	5	X	x	x	3	x	X
CODE 9	X	x	x	X	5	6	x	x	8	X	x	x	8	x	X
CODE 10	X	x	4	X	5	4	x	x	x	X	x	x	x	x	X
CODE 11	X	x	4	X	5	5	x	x	x	X	x	x	x	4	X
CODE 12	X	x	3	X	3	x	x	x	x	X	x	x	x	4	X
CODE 13	X	x	x	X	x	x	x	x	x	X	x	x	x	x	X
CODE 14	X	x	x	X	x	x	4	x	x	X	x	x	x	x	X
CODE 15	X	x	x	X	x	x	3	x	x	X	5	x	x	4	X
CODE 16	X	x	x	X	x	x	4	x	x	X	5	x	x	4	X

Fig. 6

FIG. 7



Fast DCA

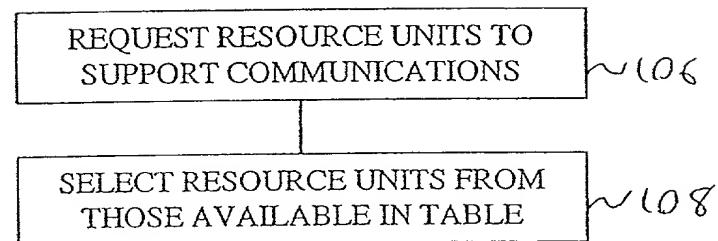


Fig. 8

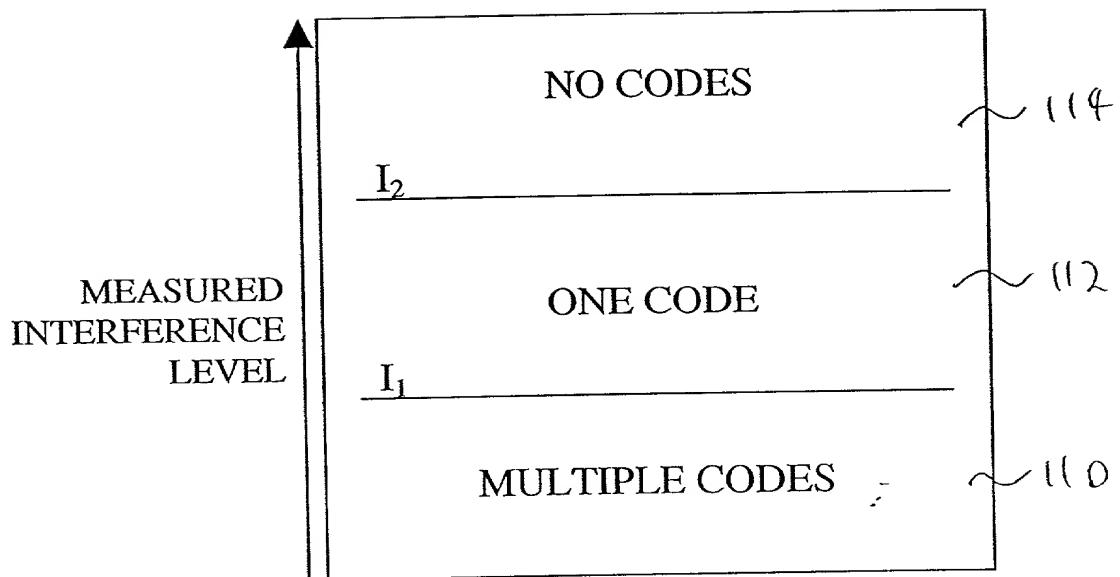


Fig. 9

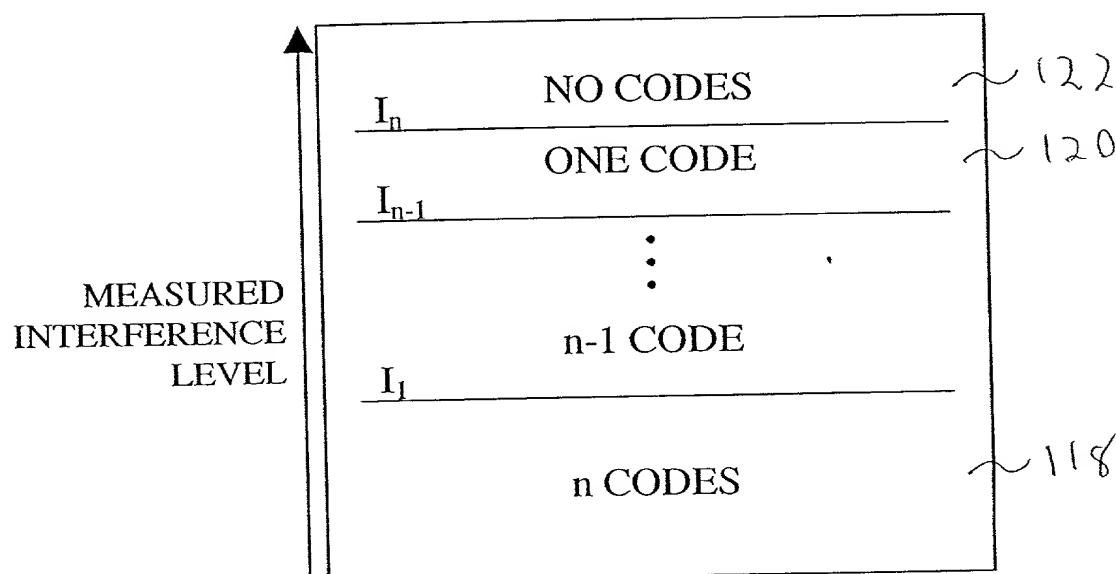


Fig. 10

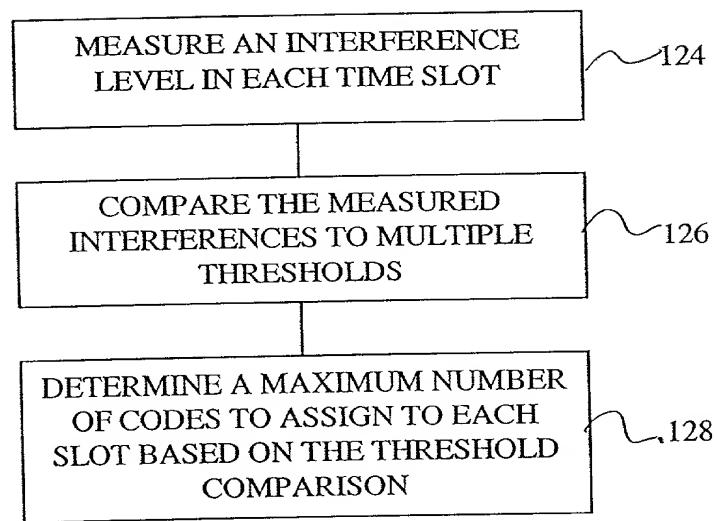


FIG. 11